

Applicant(s)	Roohparvar, et al.	INFORMATION DISCLOSURE STATEMENT FORM PTO-1449
Serial No.	09/608,256 10/670,038	
Filing Date	June 30, 2000	
Group Art Unit	2751	
Examiner Name	Unknown	
Attorney Docket No.	400.021US01	
Title: TOP/BOTTOM SYMMETRICAL PROTECTION SCHEME FOR FLASH		Sheet 1 of 1

U.S. Patent References				
Examiner Initials	Patent No.	Issue Date	Name	Filing Date
PRM	5,041,886	08/20/1991	Lee	09/25/89
	5,526,364	06/11/1996	Roohparvar	02/10/1995
	5,537,354	07/16/1996	Mochizuki et al.	12/14/1994
	5,432,927	07/11/1995	Grote et al.	11/17/1994
	5,539,894	07/23/1996	Webber	04/20/1993
	5,546,561	08/13/1996	Kynett et al.	01/13/1995
	5,566,323	10/15/1996	Ugon	10/24/1994
	5,598,548	01/28/1997	Matsuo et al.	11/03/1994
	5,600,605	02/04/1997	Schaefer	06/07/1995
	5,666,321	09/09/1997	Schaefer	09/01/1995
	5,673,222	09/30/1997	Fukumoto et al	06/20/1996
	5,701,492	12/23/1997	Wadsworth et al.	03/29/1996
	5,751,039	05/12/1998	Kauffman et al.	09/19/1997
	5,787,457	07/28/1998	Miller et al.	10/18/1996
	5,936,903	08/10/1999	Jeng et al.	08/07/1998
	5,943,263	08/24/1999	Roohparvar	01/08/1997
	5,978,273	11/02/1999	Shigemura	03/30/1998
	5,995,438	11/30/1999	Jeng, et al.	08/07/1998
	6,026,016	02/15/2000	Gafken	05/11/1998
	6,026,465	02/15/2000	Mills et al.	06/18/1997
	6,028,798	02/22/2000	Roohparvar	03/11/1997
	6,031,757	02/29/2000	Chuang et al	11/22/1996
	6,034,889	03/07/2000	Mani et al	10/23/1998
	6,137,133	10/24/2000	Kauffman et al.	05/11/1998
	6,141,247	10/31/2000	Roohparvar et al.	12/21/1998
	6,154,819	11/28/2000	Larsen et al.	05/11/1998
PRM	6,490,202	12/03/2002	Roohparvar	04/06/2001

Foreign Patent References					
Examiner Initials	Foreign Patent		Name	Publication Date	T?
	Country	No.			
PRM	EP	EP 0 806 772 A2	Motorola, Inc.	12.11.97	

Other References	
Examiner Initials	Author, Title, Date, Pages, etc.
PRM	Keeth, et al., "DRAM circuit design: a tutorial," IEEE Press, 2001, pp.16-23, 142-153
PRM	Micron Semiconductor Products, Inc., "2Mb, Smart 5 BIOS-Optimized Boot Block Flash Memory," Flash Memory www.micron.com, copyright 2000, Micron Technology, Inc., pp. 1-12
PRM	Micron, "16 Mb: x16 SDRAM" Synchronous DRAM, www.micron.com, copyright 1999 Micron Technology, Inc., pp. 1-51

Examiner Signature	Paul R. Byrne	Date Considered	5/23/05
*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			
Based on Form PTO-FB-A820 Patent and Trademark Office, U.S. Department of Commerce			